

MSD10065G1

Automotive 650V Silicon Carbide Schottky Diode

Features

- 650-Volt Schottky Rectifier
- Shorter recovery time
- High-speed switching possible
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on VF

Benefits

- Higher safety margin against overvoltage
- Improved efficiency all load conditions
- Increased efficiency compared to Silicon Diode alternatives
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses

Applications

- Switch Mode Power Supplies
- Power Factor Correction
- Motor Drives
- HID Lighting



Package

Type : TO-252

1、Cathode 2、Anode



Absolute Maximum Ratings T_c = 25°C unless otherwise noted

Symbol	Parameter	MSD10065G1	Units
VRRM	Repetitive Peak Reverse Voltage	650	V
VRSM	Surge Peak Reverse Voltage	650	V
VDC	DC Blocking Voltage	650	V
IF	Continuous Forward Current @T _c =150°C	10	A
IFRM	Repetitive Peak Forward Surge Current @TC=25°C tp = 10 ms, Half Sine Wave	80	A
IFSM	Non-Repetitive Peak Forward Surge Current @TC=25°C tp= 10 ms, Half Sine Wave	105	A
IFSM	Non-Repetitive Peak Forward Surge Current @TC=25°C, tp= 10 us, pulse	840	A
Ptot	Power Dissipation @T _c =25°C @T _c =110°C	138 60	W
T _J , T _{stg}	Operating Junction and Storage Temperature	-55 to +175	°C

Electrical Characteristics

$T_C = 25^\circ \text{C}$ unless otherwise noted

Symbol	Test Conditions	Test Conditions	Min	Typ	Max	Unit
VF	Forward Voltage	IF=10A, TC=25° C IF=10A, TC=175° C	-	1.45 1.75	1.7 2.00	V
IR	Reverse Current	VR=650V, TC=25° C VR=650V, TC=175° C	-	2 40	20 200	μA
QC	Total Capacitive Charge	VR =400V, IF =10A TJ = 25° C $Q_c = \int_0^{t_r} C (V) dt$	-	28	-	nC
C	Total Capacitance	VR =0V, TJ = 25° C, f=1MHz VR =200V, TJ = 25° C, f=1MHz VR =400V, TJ = 25° C, f=1MHz	-	550 53 48	-	pF
EC	Capacitance Stored Energy	VR=400V	-	7.0	-	μJ

Thermal Characteristics

Symbol	Parameter	Typ	Unit
RθJC	Thermal Resistance from Junction to Case	1.09	°C/W

Typical Characteristics

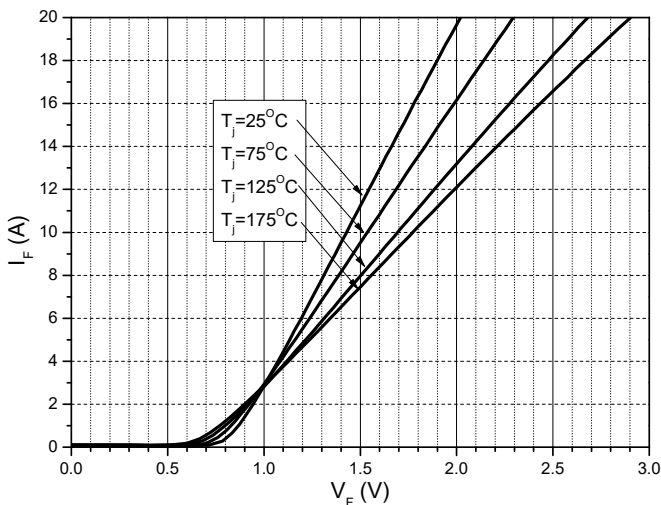


Figure 1. Forward Characteristics

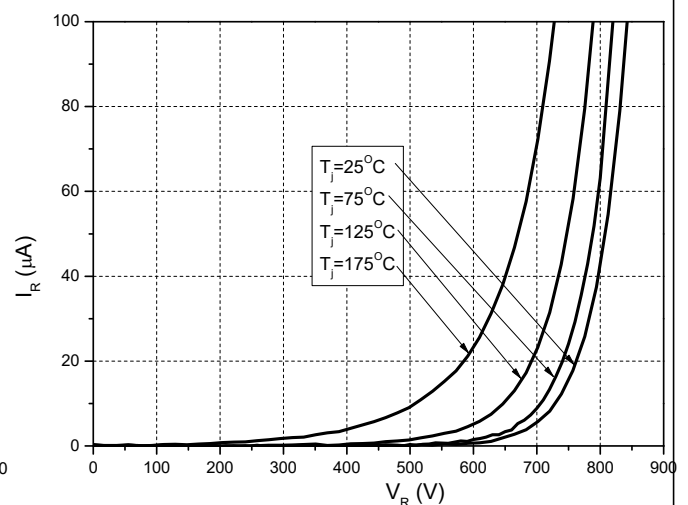


Figure 2. Reverse Characteristics

Typical Characteristics

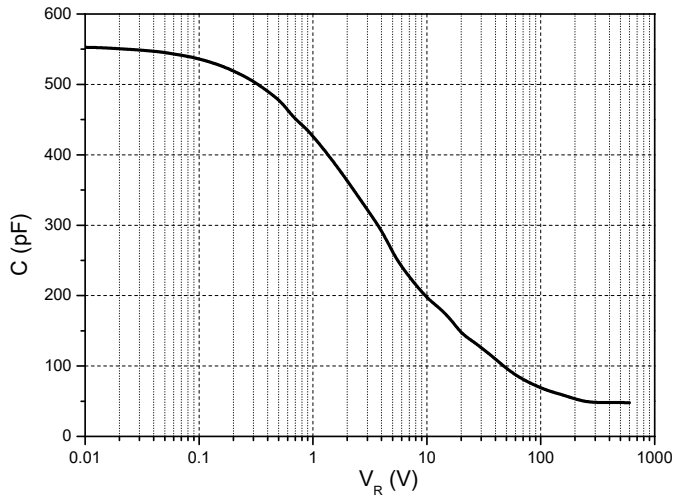


Figure 3. Capacitance vs. Reverse Voltage

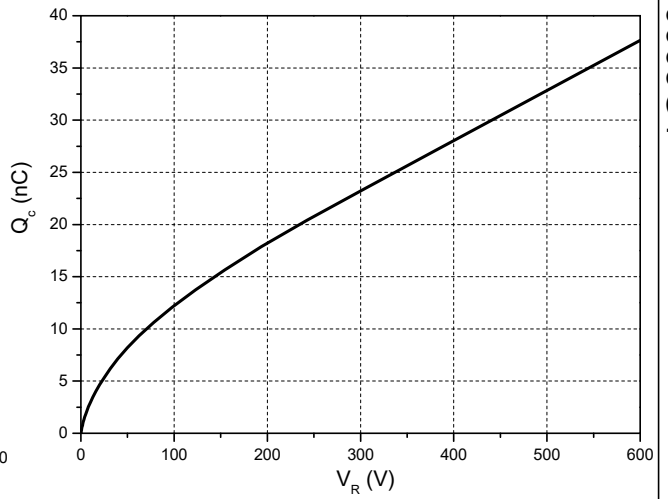


Figure 4. Total Capacitance Charge vs. Reverse Voltage

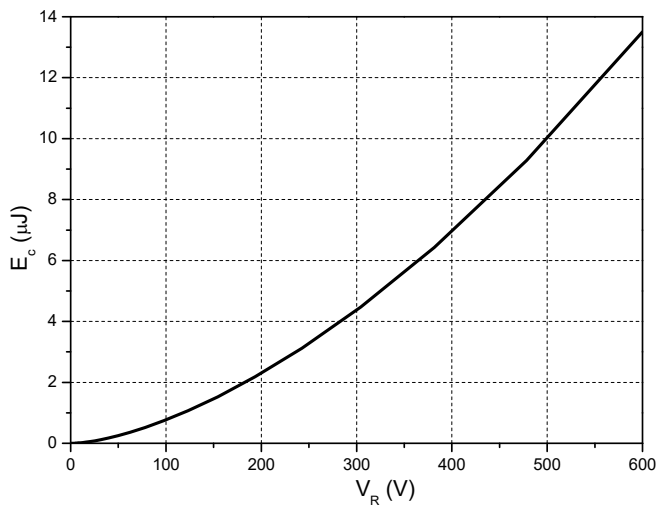


Figure 5. Capacitance Stored Energy

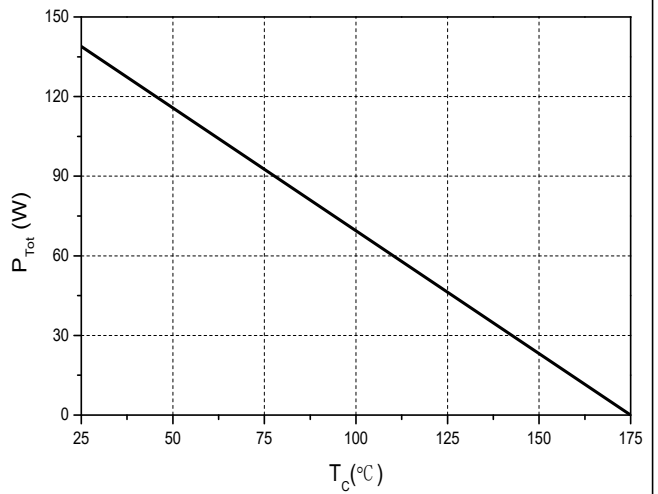


Figure 6. Power Derating

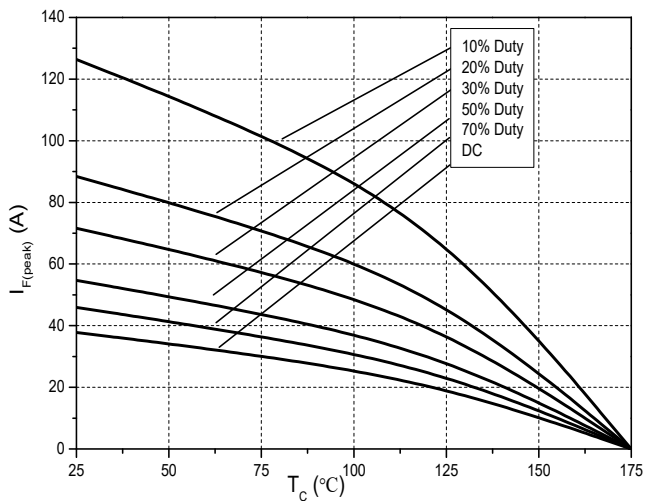


Figure 7. Current Derating

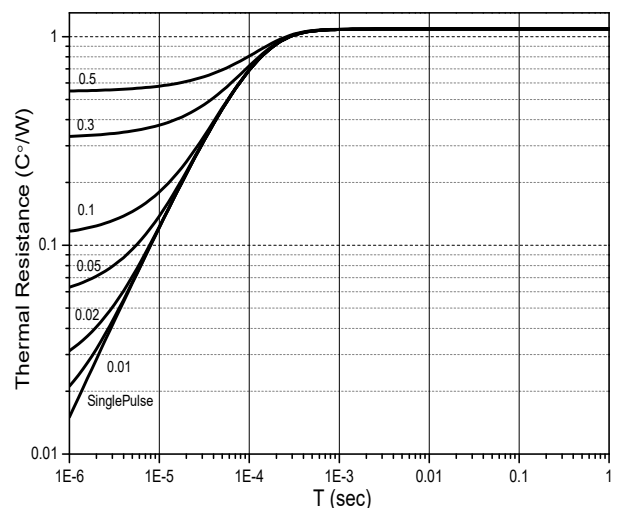
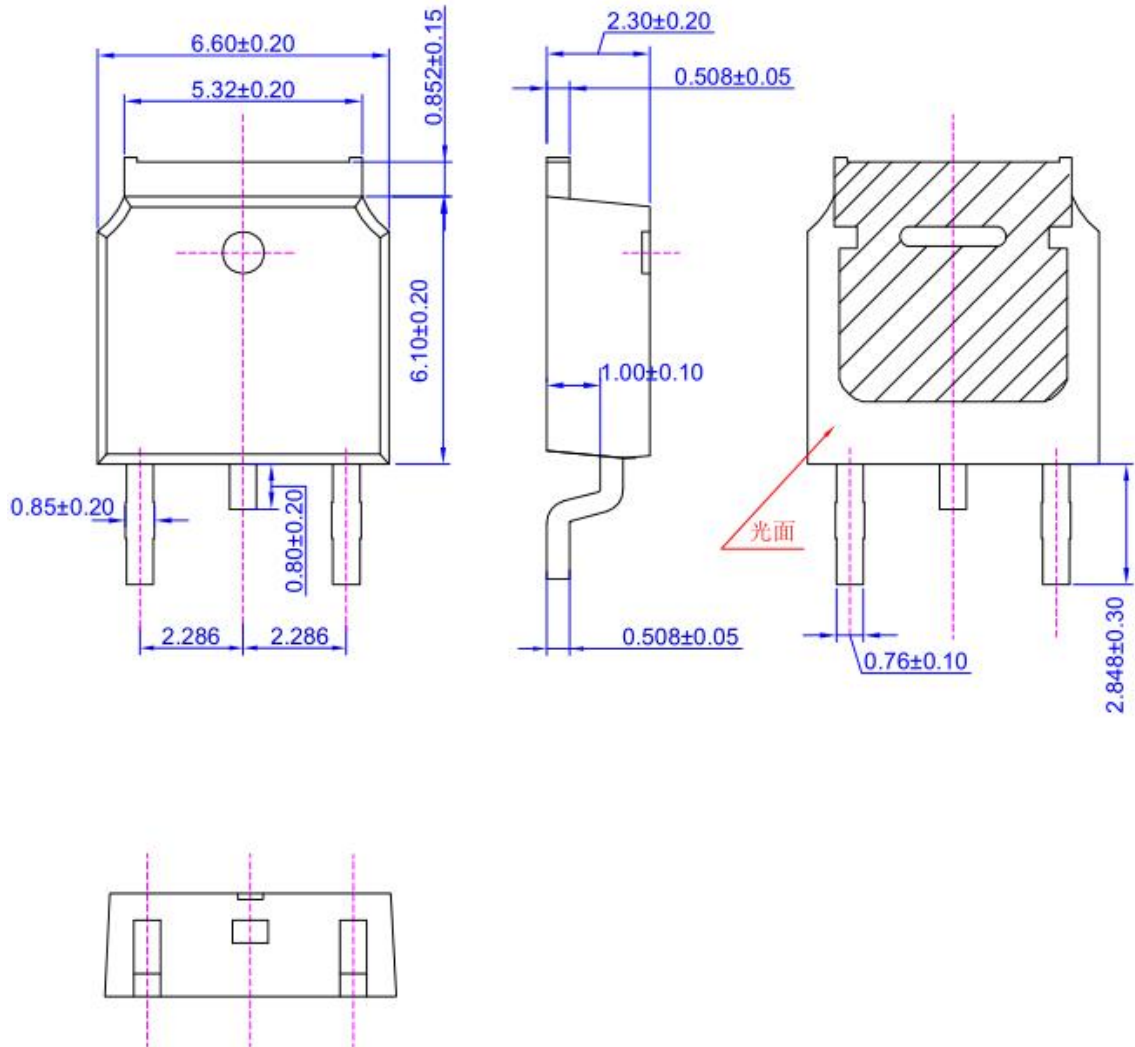


Figure 8. Transient Thermal Impedance

TO-252 OUTLINE



NOTE:

- 1The plastic package is not marked as smooth surface $Ra=0.1$; Subglossy surface $Ra=0.8$
- 2.Undeclared tolerance ± 0.25 , Unmarked fillet $R_{max}=0.25$

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